

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10802067	
	Filing Date		2004-03-16	
	First Named Inventor	Jon M. Huppenthal et al.		
	Art Unit	2819		
	Examiner Name	Chang, Daniel D.		
	Attorney Docket Number	ARB001 CON/CIP		

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	1	SAVASTIOUK, SERGEY, SINIAGUINE, OLEG, RECHE, JOHN, KORCEZYNSKI, "Thru-Silicon Interconnect Technology" Tru-Si Technologies, IEEE.CPMT Int'l Electronics Manufacturing Technology Symposium, 2000, pp. 122-128.	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/william j. kubida/	Date (YYYY-MM-DD)	2006-08-23
Name/Print	William J. Kubida	Registration Number	29664

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